

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re continuation application of:)	
V. AKELLA et al.)	Art Unit: Unassigned
)	Examiner: Unassigned
Serial No: (unassigned))	
)	
Parent Serial No: 09/187,760)	
Parent Filing Date: November 6, 1998)	
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Filed: Even Date Herewith)	
)	
For: DISTRIBUTED SWITCH MEMORY)	
ARCHITECTURE)	
)	

PRELIMINARY AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination of the above-identified application, claiming priority from U.S. patent application serial No. 09/187,760, filed November 6, 1998, please enter the following amendments and remarks.

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